



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Douglas E. Duschatko, Lane B. Quibodeaux, Robert A. Hall, Andrew J. Thurston  
Assignee: Cisco Technology, Inc.  
Title: PATH AIS INSERTION FOR CONCATENATED PAYLOADS ACROSS MULTIPLE PROCESSORS  
Serial No.: 09/607,912 Filed: June 30, 2000  
Date Allowed: November 22, 2004 Confirmation No.: 1177  
Examiner: Man Phan Group Art Unit: 2665  
Docket No.: CIS0062US

---

Austin, Texas  
February 17, 2005

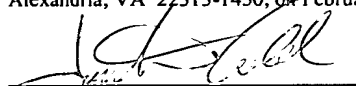
Mail Stop Issue Fee  
Attention: Official Draftsperson  
COMMISSIONER FOR PATENTS  
P. O. Box 1450  
Alexandria, VA 22313-1450

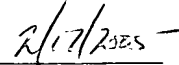
**SUBMISSION OF FORMAL DRAWINGS**

Dear Sir:

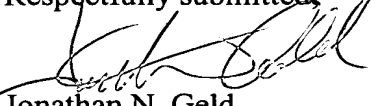
Applicants submit nine (9) sheets of formal drawings, consisting of Figures 1A & 1B; 2; 3; 4; 5A; 5B; 5C; 6; and 7 & 8 in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (512) 439-5080.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to Mail Stop Issue Fee, Attention: Official Draftsperson, Commissioner For Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on February 17, 2005.

  
Attorney for Applicant(s)

  
Date of Signature

Respectfully submitted,

  
Jonathan N. Geld  
Attorney for Applicants  
Reg. No. 44,702  
(512) 439-5080  
(512) 439-5099 (Facsimile)